Exercise 2

"Register"

Circuit

2 controls (2 wires each)
load/store (from busA)
write (to busB)
output

Assessment:

70% - correctness of circuit, etc.,
sizing
design formed properly,
no design rule errors.

30% - floorplan

100% - least area

Midnight 7th May 2006
(Sunday)

Minimum bounding box (mbb)